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Development of TaC substrates for AlGa_N power electronics

Performance of UWBG materials for vertical high-power, high-frequency electronics is characterized by the Baliga FOM which is a measure of power handling, a cubic function of critical electric field. Further scaling of power handling to 106x compared to the industry workhorse Si is limited by the availability of conducting lattice matched substrates. Conventional substrate materials that can be grown in bulk are either lattice mismatched to the best-known active layers (e.g., Al₂O₃ is lattice mismatched for AlGa_N), lack a good combination of electrical and thermal properties (e.g., Ga₂O₃), or are cost prohibitive (e.g., diamond). High electrical and thermal conductivity of the substrate leads to improved device functionality and reliability with lower electrical losses, higher switching frequencies, and more robust performance under extreme conditions. We will discuss how the power handling of Al_{0.7}Ga_{0.3}N (001) devices on lattice matched TaC (111) with measured resistivity $\sim 200 \mu\Omega/\square$ $\ll 10 \mu\Omega/\square$ for SiC substrates could lead to 100X power handling compared to similar GaN/SiC devices. We show rectification in lattice matched AlGa_N/TaC Schottky diodes with measured critical fields $> 4 \text{ MV/cm}$ higher than GaN/SiC. Use of TaC substrates could enable thick unstrained AlGa_N drift layers to be realized for high voltage devices $> 3 \text{ kV}$ and high current handling $> 10 \text{ kA/cm}^2$.

Academic or Professional Status

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